



Docket No.: 1614.1074

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideo MIYAKE et al.

Serial No. 09/654,527

Group Art Unit: 2181

Confirmation No. 7021

Filed: September 1, 2000

Examiner: Tonia L. Meonski

For: PARALLEL PROCESSOR EFFICIENTLY EXECUTING VARIABLE INSTRUCTION
WORD (as amended)

AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed December 27, 2006, and having a period for response set to expire on March 27, 2007. A Petition for a one-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to April 27, 2007.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

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IN THE TITLE:

Please REPLACE the Title of the Invention with the following paragraph:

~~SELECTIVE INSTRUCTION ISSUING PARALLEL PROCESSOR PARALLEL~~
PROCESSOR EFFICIENTLY EXECUTING VARIABLE INSTRUCTION WORD